LD-SRU Card

Features

- **Enables connectivity to low speed V24/V28 (RS232) based devices.**
- **V24/V28 (RS232) ports can be independently programmed for synchronous or asynchronous operation.**
- **Low delay for mission critical applications.**
- **Integral testing capabilities include extensive Bit Error Rate Test (BERT) and loopback options on a per port basis.**
- **High port density for maximum peripheral connectivity.**
- **Supports industry standard subrate multiplexing schemes including DS0-A, DS0-B and X.50 Div 3**

The LD-SRU card allows connection of up to ten (10) RS-232, low speed and medium speed data terminals to the IMACS. The functionality of the new LD-SRU card (822570) is the same as for the existing LD-SRU card (PRM-822560), including the low delay throughput. The maximum of 3 ms +/- 10% end to end of rates 9.6Kbps or higher for sync and the maximum of 6 ms +/- 10% end to end of rates 9.6Kbps or higher for async.

The LD-SRU card allows connection of up to 10 RS-232, low-speed and medium-speed (300 kbps to 38.4 kbps) data terminals to the IMACS. A number of those devices can be multiplexed onto a single DS0 time slot of a WAN card. The card ports can also be multiplexed with voice traffic on an ADPCM card.

Each port of the LD-SRU card receives timing from either the external DTE or the IMACS clock. If the DTE supplies the transmit timing, that clock signal must be synchronized to the IMACS clock source.

The LD-SRU card allows connection of up to ten (10) RS-232, low-speed and medium-speed data terminals to the IMACS. This is the third time this is stated?

LD-SRU Rates:

frame	*sync (kbps)*	*async (kbps)*	*v.14 (kbps)*	*subrate timeslot*
a	2.4, 4.8, 9.6 19.2, 28.8, 38.4	0.3, 1.2, 2.4 4.8, 9.6, 14.4 19.2, 28.8, 38.4	0.3, 1.2, 2.4 4.8, 9.6, 14.4 19.2, 28.8, 38.4	1
b-5	2.4, 4.8, 9.6 19.2, 28.8, 38.4	0.3, 1.2, 2.4 4.8, 9.6, 14.4 19.2, 28.8, 38.4	0.3, 1.2, 2.4 4.8, 9.6, 14.4 19.2, 28.8, 38.4	5
Hlink	2.4, 4.8, 9.6 14.4	0.3, 1.2, 2.4 4.8, 9.6, 14.4	N/A	N/A
ADPCM	2.4, 4.8, 9.6 14.4	0.3, 1.2, 2.4 4.8, 9.6, 14.4	N/A	N/A

LD-SRU Card

Number of Ports:	10	
Frame:	a, b, Hlink, ADPCM	
Interface:	async, sync, v.14	
Interface Connector	RJ 48	
Electrical Interface		
RS-232E	+/- 12.0V, 3K < Ohms < 7K	
Character Format:	8/7/6/5 bit data, 2/1 stop bit, none/even/odd parity	
Throughput Delay:	Maximum of 3 ms +/- 10 % end to end of rates 9.6 kbps or higher for sync. Maximum of 6 ms +/- 10 % end to end of rates 9.6 kbps or higher for async.	
Majority Voting:	Only for “a” frame.	
Loopback:	Supports CSU, DSU, and OCU.	
Communication Configuration:	CTS - off, perm, local - 0, 30, 60, 100 ms, Remote - 0, 30, 60, 100 ms. Tx Clock - int, ext.	
BERT:	mark, space, 1:1, 1:7, 511, 2047.	
Control:	Configuration control via local terminal or remote NMS	
Standards:	Telcordia GR-63-CORE Issue 1, AT&T TR54075, ITU V.24, V.28, X.50, V.14, EIA RS232-C, UL 1950, CEN EN 500 81-1, EN 500 082-1, EN 60 950/A2	
Physical Specification	Card height	8 inches (20cm)
	Card width	15/18 inches (2.35 cm)
	Card depth	71/2 inches (18.75cm)
	Power consumption	1.6 Watts
	BTU/hr	5.46
	Op temperature	0 to 50 C, 32 to 122 F
	Storage temperature	-20 to 80 C, -4 to 176 F
	Humidity	0 to 95% humidity, non-condensing
IMACS Platform	IMACS chassis	891630 IMACS 600, 891830 MACS 800, or 891930 IMACS 900
	Control CPU card	880460 bus-connect or 880370 cross-connect CPU
	System Host Code	3.x.y & 6.x.y or later
	Interface card	
	Release 3.x.y	892060
	Release 6.x.y	892260, 892360 & 892460.
	Power supply options	All AC and DC power supplies supported